

VME64 Extension J1/J2 - N Series

Backplane

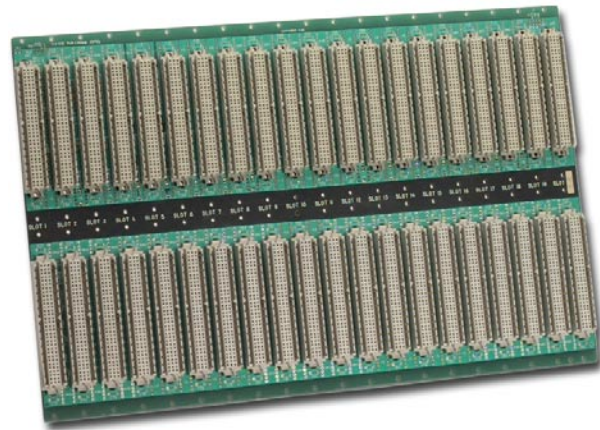
ELECTRONIC PACKAGING DIVISION

To increase the performance of a VME64 system, several extension specifications have been developed by the VITA Standards Organization (VSO). These extensions have formed the newly created VME64 Bus Extensions VITA 1.1-199x Specification. The extension specifications added the following features to the backplane:

- 160 pin DIN connector
- The pre-mate pin lengths to support the live insertion Enhanced Transceiver Logic (ETL) devices
- 3.3 Volt power pins
- +V and -V power pins
- 35 more signal ground return pins
- 46 more user defined I/O pins
- 2 bussed spare pins and 2 unbussed spare pins on the P1/J1 connector
- A test sub-bus for test, maintenance and diagnostics of VME64 boards
- Geographical addressing

These features provide the user with all the necessary enabling technologies to create a high availability system using a set of standard VME components.

Carlo Gavazzi is a member of the VSO Committee and is involved in the development of the VME64 Extension Specification. Carlo Gavazzi's staff regularly attends the VSO meetings so Carlo Gavazzi's customers can be assured that they will get the most up-to-date VME64 Extension products that are available.



Carlo Gavazzi has available a VME64 Extension Backplane that has the latest pin out and supporting electronics to provide the customer a working backplane. This VME64 Extension Backplane has the following features:

- 100% SMT of all electronic components
- All press-fit connectors
- Long tail 160 pin DIN connectors and shrouds in all the J2 positions are available
- Power entry is provided through 6 pin power blocks
- A 10 pin latching header provides connection to ACFAIL, RESET, SYSFAIL, GND
- 3 to 21 slots

Carlo Gavazzi's N Series VME64 Extension Backplane will be continuously updated as the VSO Committee develops this specification. Please call Carlo Gavazzi or your local area Carlo Gavazzi Representative for the latest information on this product.

—innovative engineering, dependable products—

Carlo Gavazzi Computing Solutions designs and manufactures standard and custom AdvancedTCA™, CompactPCI®, PCI/ISA, and Switch Fabric backplanes, cabinets, enclosures, I/O boards, and integrated systems that address your outsourcing and time-to-market needs.



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VME64 Extension J1/J2 - N Series

Backplane

ELECTRONIC PACKAGING DIVISION

Ordering Information — N Series VME J1/J2

Model Number Selection: N

Option Code _____ Slot Count _____

Additional Features	Option Code			
	F	G	K	P
1. Shrouds on rear of all J2 and first and last J1 connectors, plus 10 pin latching header (AMP746140 or equiv.) on ACFAIL, SYSFAIL, SYSRESET, GND, +5V, +12V, -12V, 3.3V			√	√

Ordering Examples:

NF09 = 9 slot, 10 layer, VME 64 J1/J2 base line standard w/o cost adder options
 NL20 = 20 slot, 10 layer, VME 64 J1/J2 with shrouds and 10 pin header, plus automatic IACK and BG jumpering

Model No.	Slot Count	Dim A	Power Block Locations*								GND
			+5	+3.3V	+12	-12	+5B	+V	-V		
NX09	9	7.14 (181,43)	B1, B2, B3, B6, B7, B8	—	B4	C4	B5	A6	A5	C1, C2, C3, C5, C6, C7, C8, D1-D8	
NX10	10	7.94 (201,68)	B1, B2, B3, B7, B8, B9	—	B5	C5	B6	A7	A6	C1, C2, C3, C4, C6, C7, C8, C9, D1-D9	
NX12	12	9.54 (242,39)	B1, B2, B3, B4, B5, B8, B9	—	B6	C6	B7	A6	A5	C2, C3, C4, C5, C7, C8, C9, C10, D1-D11	
NX16	16	12.74 (323,67)	B1, B2, B3, B4, B5, B6, B10, B11, B12, B13	—	B8	C8	B9	A7	A6	C3, C4, C5, C6, C10, C11, C12, C13, C14, D1-D15	
NX20	20	15.94 (404,95)	B1, B2, B3, B4, B5, B6, B7, B13, B14, B15, B16, B17	—	B9, B10	C9, C10	B11, B19	A9, A10	A7, A8	C3, C4, C5, C6, C7, C12, C13, C14, C15, C16, C17, C18, D1-D19	
NX21	21	16.74 (425,27)	B1, B2, B3, B4, B5, B6, B7, B8, B14, B15, B16, B17, B18	—	B10, B11	C10, C11	B12, B20	A10, A11	A8, A9	C3, C4, C5, C6, C7, C8, C13, C14, C15, C16, C17, C18, D1-D20	

*15 Amp Max Input per Power Block (Fully Derated @ 60°C)
 **See Accessory Section for Bus Bar Drawings

NOTE: VME 64 Pin Designation Chart and Design Guides for Maximum Power per Slot can be found in the Appendix section of the catalog

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